

Refine Search

Search Results -

| Term | Documents |
|--|-----------|
| HARDWARE | 379104 |
| HARDWARES | 1746 |
| REQUEST\$3 | 0 |
| REQUEST | 380906 |
| REQUESTA | 2 |
| REQUESTACK | 2 |
| REQUESTAI | 1 |
| REQUESTAND | 4 |
| REQUESTARS | 2 |
| REQUESTASP | 1 |
| REQUESTAUX | 2 |
| (REQUEST\$3 NEAR35 HARDWARE NEAR1 ACCELERAT\$4).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD. | 72 |

[There are more results than shown above. Click here to view the entire set.](#)

| | |
|--|--|
| Database: US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins | Search: <input type="text" value="5872993.pn. and request\$3"/> <div style="display: flex; justify-content: space-between; width: 100%;"> <div style="flex: 1;"> <input type="button" value="Recall Text"/> </div> <div style="flex: 1;"> <input type="button" value="Clear"/> </div> <div style="flex: 1;"> <input type="button" value="Interrupt"/> </div> </div> <div style="text-align: right; margin-top: 10px;"> <input type="button" value="Refine Search"/> </div> |
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Search History

DATE: Friday, November 12, 2004 [Printable Copy](#) [Create Case](#)

Set
Name Query
 side by
 side

Hit
Count
Name
 result
 set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

| | | | |
|------------|---|----|------------|
| <u>L27</u> | request\$3 near35 hardware near1 accelerat\$4 | 72 | <u>L27</u> |
| <u>L26</u> | L25 not l18 | 12 | <u>L26</u> |
| <u>L25</u> | request\$3 near25 hardware near1 accelerat\$4 | 72 | <u>L25</u> |
| <u>L24</u> | l18 and l7 | 7 | <u>L24</u> |
| <u>L23</u> | (wait\$3 or stand\$3 near2 by) near8 (simd or single near1 instruction near3 multipl\$7) | 50 | <u>L23</u> |
| <u>L22</u> | (size or length or width) near6 (equal\$4 or fixed or "SAME") near8 packet\$1 and (simd or single near1 instruction near3 multipl\$7) | 52 | <u>L22</u> |
| <u>L21</u> | 4922487.pn. and (simd or single near1 instruction near3 multipl\$7) | 0 | <u>L21</u> |
| <u>L20</u> | (divid\$5 or division\$1 or partition\$6 or broken or break\$4) near7 (fixed or equal\$4) near6 packet\$1 and (simd or single near1 instruction near3 multipl\$7) | 1 | <u>L20</u> |
| <u>L19</u> | (divid\$5 or division\$1 or partition\$6 or broken or break\$4) near7 (fixed or equal\$4) near6 packet\$1 and (simd or single near1 instruction near3 multipl\$7) | 1 | <u>L19</u> |
| <u>L18</u> | request\$3 near15 hardware near1 accelerat\$4 | 60 | <u>L18</u> |
| <u>L17</u> | request\$3 near15 hardware near1 accelerat\$4 near20 process\$3 near2 (unit\$1 or element\$1) | 6 | <u>L17</u> |

DB=PGPB,USPT; PLUR=YES; OP=OR

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|------------|---|-------|------------|
| <u>L16</u> | request\$3 near15 hardware near1 accelerat\$4 and l11 | 1 | <u>L16</u> |
| <u>L15</u> | request\$3 near15 hardware near1 accelerat\$4 and l9 | 7 | <u>L15</u> |
| <u>L14</u> | request\$3 near15 hardware near1 accelerat\$4 and l8 | 7 | <u>L14</u> |
| <u>L13</u> | request\$3 near15 hardware near1 accelerat\$4 and l7 | 7 | <u>L13</u> |
| <u>L12</u> | (712/20-23, 225, 227)![CCLS] | 1046 | <u>L12</u> |
| <u>L11</u> | (712/20-23)![CCLS] | 1046 | <u>L11</u> |
| <u>L10</u> | (712/2-43)[CCLS] | 4317 | <u>L10</u> |
| <u>L9</u> | (712/2-43)![CCLS] | 4317 | <u>L9</u> |
| <u>L8</u> | (712/2-300)[CCLS] | 10433 | <u>L8</u> |
| <u>L7</u> | (712/2-300)![CCLS] | 10433 | <u>L7</u> |

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|-----------|--|-----|-----------|
| <u>L6</u> | request\$3 near15 hardware near1 accelerat\$4 and (simd or single near1 instruction near2 multipl\$7) | 8 | <u>L6</u> |
| <u>L5</u> | request\$3 near8 hardware near1 accelerat\$4 and (simd or single near1 instruction near2 multipl\$7) | 8 | <u>L5</u> |
| <u>L4</u> | L3 and hardware near1 accelerat\$4 | 6 | <u>L4</u> |
| <u>L3</u> | (simd or single near1 instruction near2 multipl\$7) and process\$3 near1 element\$1 and request\$1 near15 (result\$1 or return\$4) | 123 | <u>L3</u> |
| <u>L2</u> | (simd or single near1 instruction near2 multipl\$7) and process\$3 near1 element\$1 and request\$1 near8 (fifo\$1 or wait\$4 or priorit\$7) near8 (result\$1 or return\$4) | 1 | <u>L2</u> |
| <u>L1</u> | (simd or single near1 instruction near2 multipl\$7) and process\$3 near1 element\$1 near8 request\$1 near8 (fifo\$1 or wait\$4 or priorit\$7) near8 (result\$1 or return\$4) | 0 | <u>L1</u> |

END OF SEARCH HISTORY

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